

School of Engineering**B.TECH Electronics and Communication Engineering
Semester End Examination - Jul 2024**

Duration : 180 Minutes
Max Marks : 100

Sem IV - G2UA401C - Microcontrollers and Embedded SystemGeneral Instructions

Answer to the specific question asked

Draw neat, labelled diagrams wherever necessary

Approved data hand books are allowed subject to verification by the Invigilator

- 1) Define Mode Set Register (MSR) in the context of the 8255 Programmable Peripheral Interface (PPI). K1(2)
- 2) Explain about Port-0 Pins of 8051. K2(4)
- 3) Explain the difference between Register and Register Indirect addressing of 8086 with example. K2(6)
- 4) (a) Illustrate how A0 and BHE' signals used to select the memory banks of 8086 physical memory. (b) illustrate the significance of ALE signal with a neat diagram in general bus operations. K3(9)
- 5) Illustrate arithmetic instructions in 8051. K3(9)
- 6) Examine TCON Registers in 8051. K5(10)
- 7) Analyze the difference between call and jump instructions in 8051 with examples. K4(12)
- 8) Examine different pin of 8086 microprocessor. K5(15)
- 9) Discuss about the various types of microcontrollers. K5(15)
- 10) Elaborate about the flag register and their functions in 8086 microprocessor. K6(18)