

School of Engineering

M.Tech Power System Engineering Semester End Examination - Jun 2024

Duration: 180 Minutes Max Marks: 100

Sem II - G2PI205C - Power Quality

General Instructions

Answer to the specific question asked
Draw neat, labelled diagrams wherever necessary
Approved data hand books are allowed subject to verification by the Invigilator

1)	Define purpose of digital fault recorders.	K1(2)
2)	Explain the long duration voltage variations.	K2(4)
3)	Explain the effect of line drop compensation on the voltage profile.	K2(6)
4)	Illustrate various instruments used for power quality	K3(9)
	measurements.	
5)	Illustrate Sharpness of Tuning of Passive Filters.	K3(9)
6)	Examine the effects of harmonic distortion on transformers and motors.	K5(10)
7)	Analyze the process of calculating the symmetrical components of voltage and current during a faulted condition, and how are these components used in power quality assessment.	K4(12)
8)	Examine the steps involved in power quality monitoring and the information's from monitoring site survey.	K5(15)
9)	Examine about the Configuration, Structure and Control of UPQC.	K5(15)
10)	Discuss about IEEE and IEC Standards used for power quality issues and Describe the objective of power quality standards.	K6(18)