# School of Engineering

B.TECH Electronics and Communication Engineering
Mid Term Examination - Nov 2023

**Duration : 90 Minutes**
Max Marks : 50

Sem III - G2UB301B - Network Analysis and Synthesis

General Instructions
Answer to the specific question asked
Draw neat, labelled diagrams wherever necessary
Approved data hand books are allowed subject to verification by the Invigilator

1) How do you analyze an electrical circuit?   K2 (2)

2) Define star -delta conversion and explain it with examples.   K1 (3)

3) Explain linear and non linear network with example.   K2 (4)

4) Illustrate concept of Tellegens theorem with applications.   K2 (6)

5) Identify dual networks with example.   K3 (6)

6) Solve the admittance parameters of T network as shown in figure.   K3 (9)
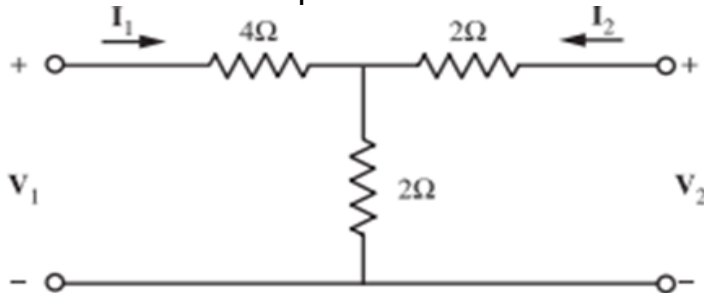
7) Simplify voltage across 4 ohm resister using superposition theorem .   K4 (8)
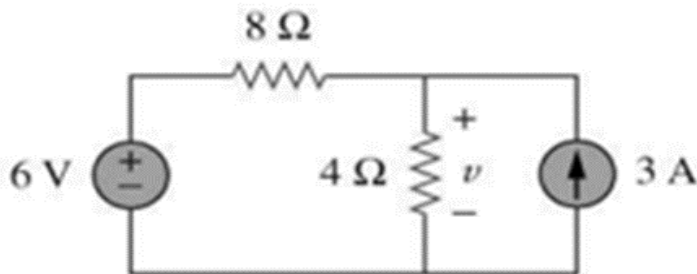
8) Examine coversion of y parameter to z parameter.   K4 (12)

OR

Examine driving point impedance and admittance.   K4 (12)