

# School of Computing Science and Engineering

B.Tech CSE  
ETE - Jun 2023

Time : 3 Hours

Marks : 50

## Sem II - MSCS1210 - Computer Architecture

*Your answer should be specific to the question asked*

*Draw neat labeled diagrams wherever necessary*

1. Outline different kinds of ROMs. K3 CO2 (2)
2. Give the control sequence for execution of an instruction. K1 CO1 (2)
3. Enlist different types of memory. K4 CO2 (2)
4. What is the function of cache memory? K2 CO1 (2)
5. List the types of Computers. K4 CO2 (2)
6. Convert decimal number 142 into BCD and Octal number system. K4 CO2 (5)
7. Analyze the importance of data paths and control flow for sequencing in CPUs with the help of diagrams. K6 CO2 (6)
8. Draw a flow chart for the execution of  $(A+B)*(C+D)$ . K3 CO2 (5)
9. Distinguish between the write-through and write-back policies pointing out their merits and demerits. K4 CO2 (8)
10. Discuss the need of some bits of current micro-instruction to generate address of the next micro-instruction? Support with a live example K5 CO3 (8)
11. Discuss the use of pipelining concept in real world with the help of example. K5 CO3 (8)